# A Model-Based Design approach for embedded system development on STM32 microcontrollers



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## **OBJECTIVES**

A new software tool that supports Model Based Design (MBD) is presented. It is suitable for running Simulink® application models for STM32 MCUs. The first Simulink® blockset library for STM32 peripherals allows us to implement Processor In the Loop (PIL) configuration and automatic code generation. The second Simulink® blockset includes extensive Math and Motor control functions based on the STM32 Motor control library.

#### **MODEL-BASED DESIGN WORKFLOW**

MBD modifies traditional methods adopted in model development processes and introduces a better way to implement the following workflow:



### **DEVELOPMENT PROCESS**

MBD employs the V-model development process illustrated below in its various phases. The V-model allows:

- Executable Specifications
- · Design with Simulation
- Continuous Test and Verification
- Automatic Code Generation



STM32 MC motor control firmware (X-CUBE-MCSDK) also includes the Permanent-magnet synchronous motor (PMSM) firmware library (FOC control) widely used in high-performance drives

The Simulink® library provided by ST is based on C legacy code firmware to optimize runtime execution and memory footprint.

#### PARTITIONING PRINCIPLE FOR EMBEDDED SYSTEM MODELING



An MBD system can be conceptually divided into two main models: one is for simulation, based on algorithm/math blocks and functional emulation peripherals; the second is based on the same algorithm blocks plus the target-oriented peripheral driver blocks to generate auto-code.

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## MBD TOOLS FOR STM32 MCUS AND FOC MOTOR CONTROL



#### ONE MODEL FOR SIMULATION AND AUTO-CODE GENERATION





SYSTEM MODEL PARTITIONING

The key for building a successful MBD platform is partitioning the system model and the embedded software code.

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	Block		Description	Simulation	Code generation
	Electromechanical system		Averaged functional model	<ul> <li>Image: A second s</li></ul>	×
	<ul> <li>Sensor and conditioning</li> <li>Driving circuit</li> </ul>		Fine-grained function model	<ul> <li>Image: A second s</li></ul>	×
	Processor	Peripherals	Fine-grained function model	<ul> <li>Image: A second s</li></ul>	<ul> <li>Image: A second s</li></ul>
		Algorithms	Core algorithm	<ul> <li>Image: A second s</li></ul>	<ul> <li>Image: A second s</li></ul>

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